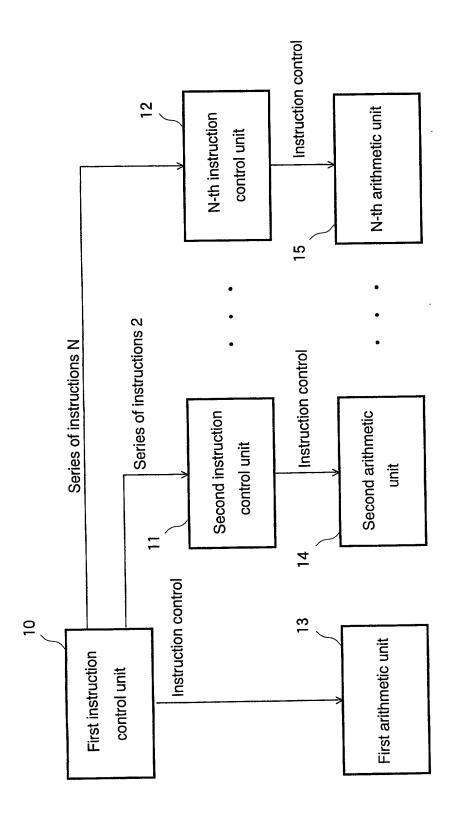
F I G. 1



F1G. 2

First instruction memory

Address	Data
0000	Series of instructions 1
1000	Series of instructions 2
0005	Series of instructions 1
0003	Series of instructions 2 (Independent execution switching)
0004	Series of instructions 1
2000	Series of instructions 1
9000	Series of instructions 1
2000	Series of instructions 1
8000	Series of instructions 2

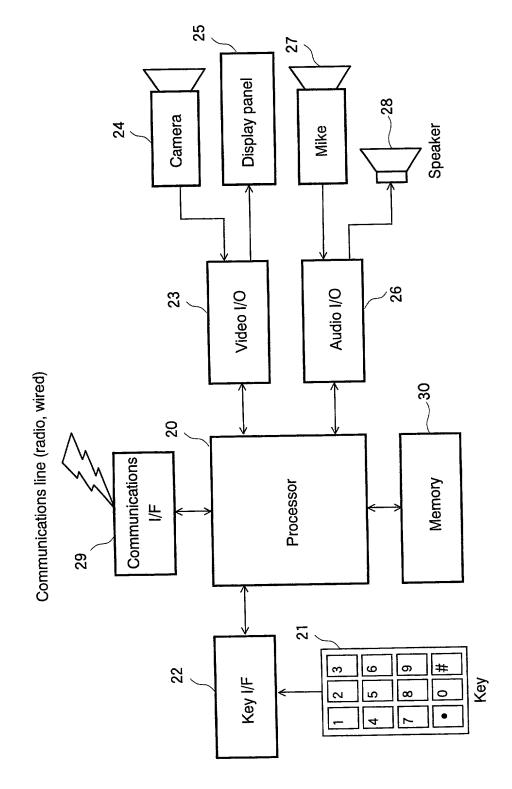
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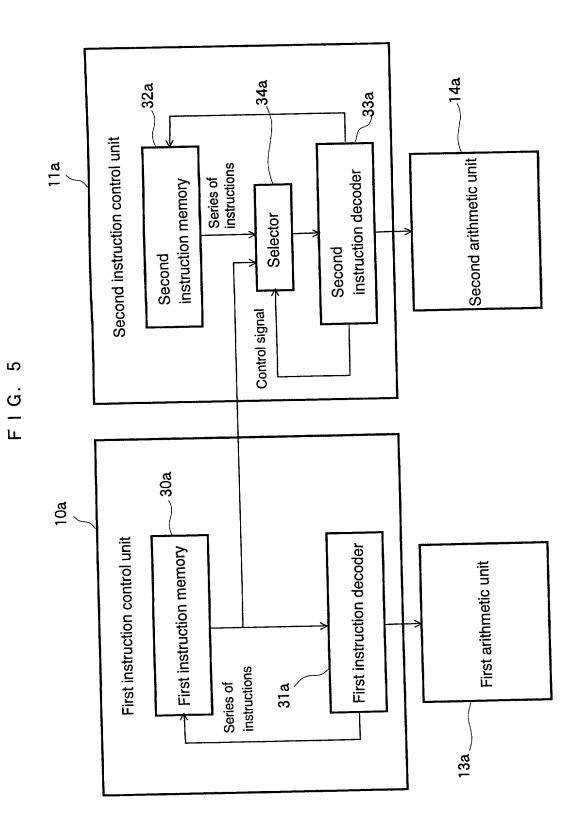
			(Bu
Data	Series of instructions 2+	Series of instructions 2+	Series of instructions 2+ (Synchronous execution switching)
Address	0000	1000	0005
<del></del>	<u> </u>		

F I G. 3

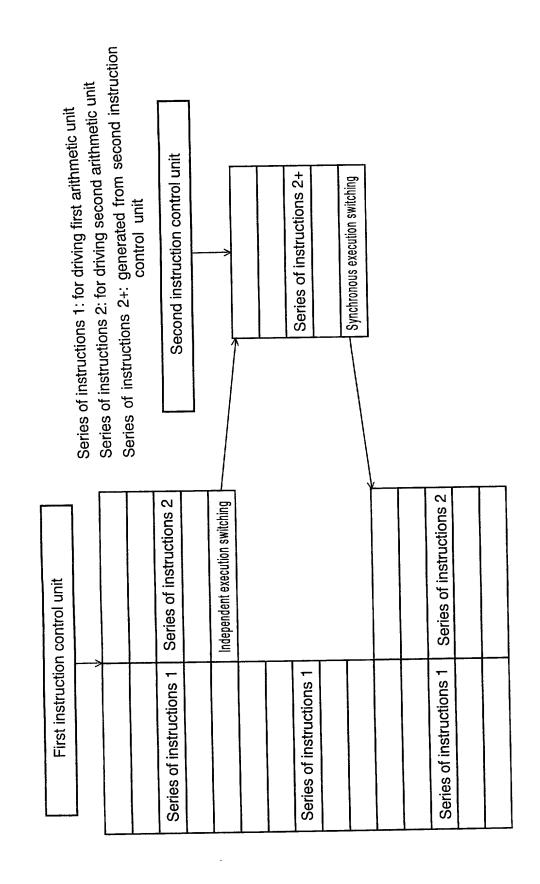
Fine granularity	Instruction level	· ADD A,B,C: Addition · MUL A,B,C: Multiplication
\	Function level	・fft(x,y):Fourier transform ・fir(x,y):FIR filter
	Application level	<ul><li>MPEG Video (Image CODEC)</li><li>GSM-AMR(Voice CODEC)</li></ul>
Coarse granularity	System level	Portable terminal system

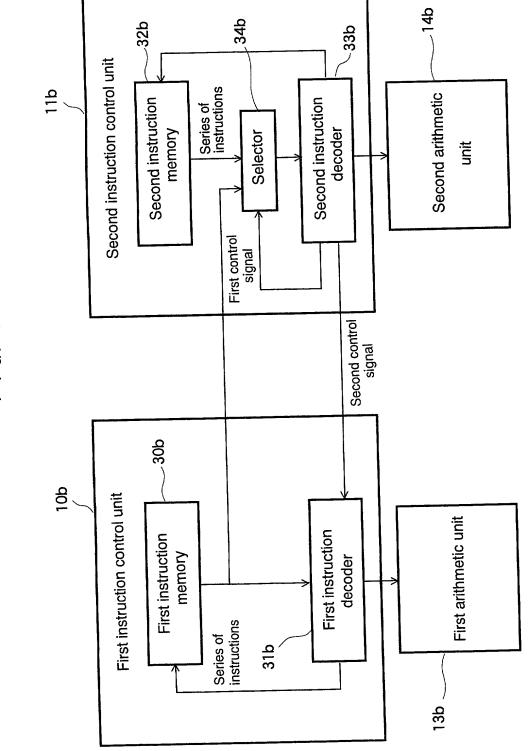
F I G. 4





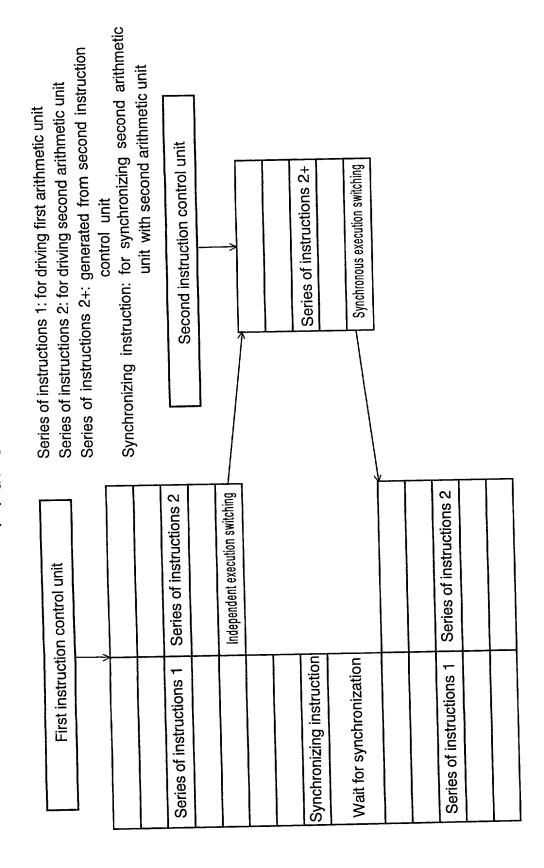
F I G. 6

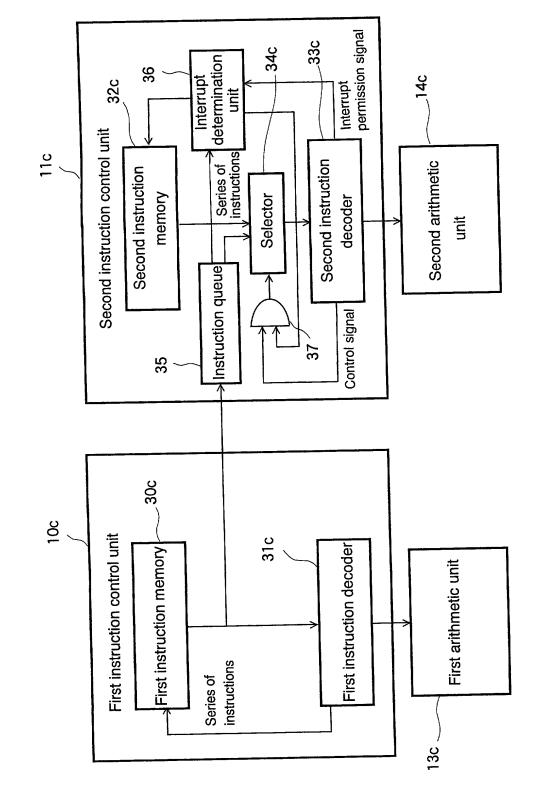




F I G. 7

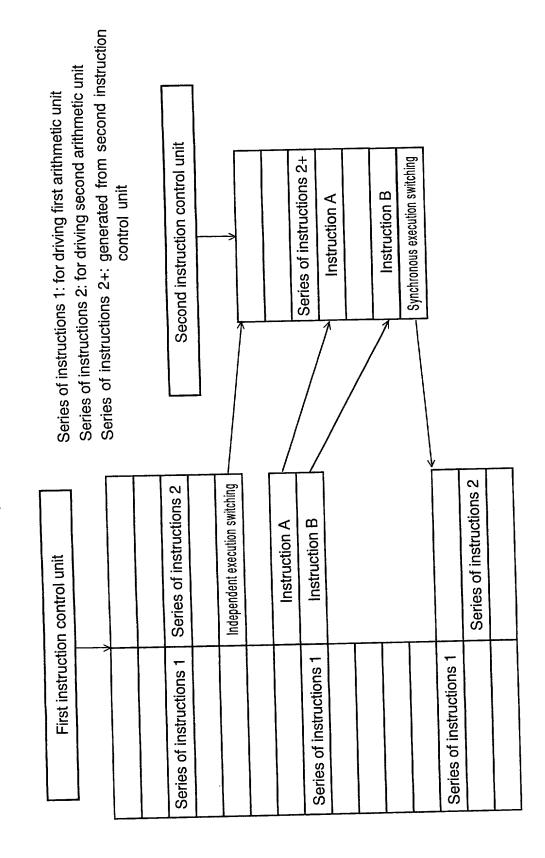
F | G. 8





F I G. 9

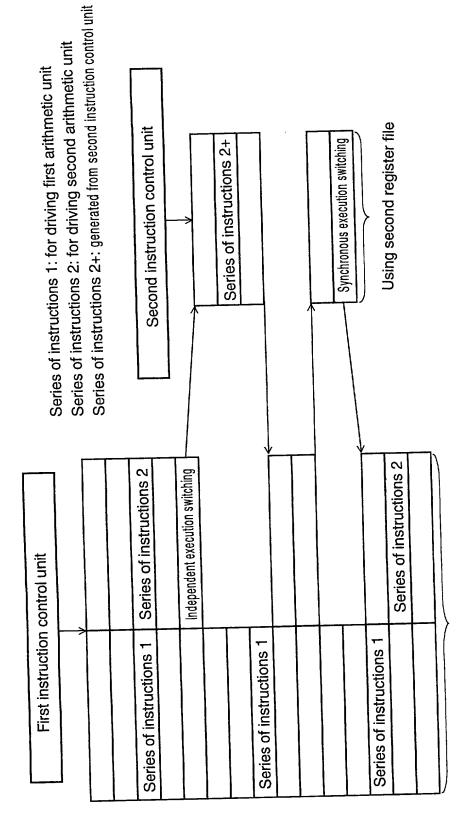
FIG. 10



14d .1d Control signal Second register file 42 Series of instructions Second instruction control unit Arithmetic unit selector Second instruction decoder Second arithmetic unit Second instruction memory Calculator Selector Instruction control First register file 34d 4 33d 32d 4 Series of instructions Instruction control 10d First arithmetic unit First instruction control unit

FIG. 11

F1G. 12



Using first register file

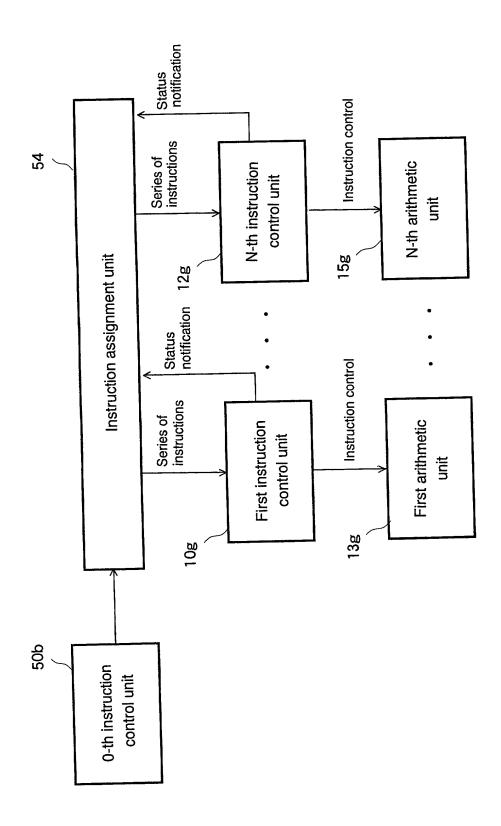
Instruction control 15e 12e Series of instructions N N-th arithmetic unit 51 N-th instruction control unit VLIW type series of instructions 10e 31e Instruction control Series of instructions 1 First arithmetic unit 34e First instruction decoder Selector 20 First instruction 0-th instruction control unit memory First instruction control unit 30e

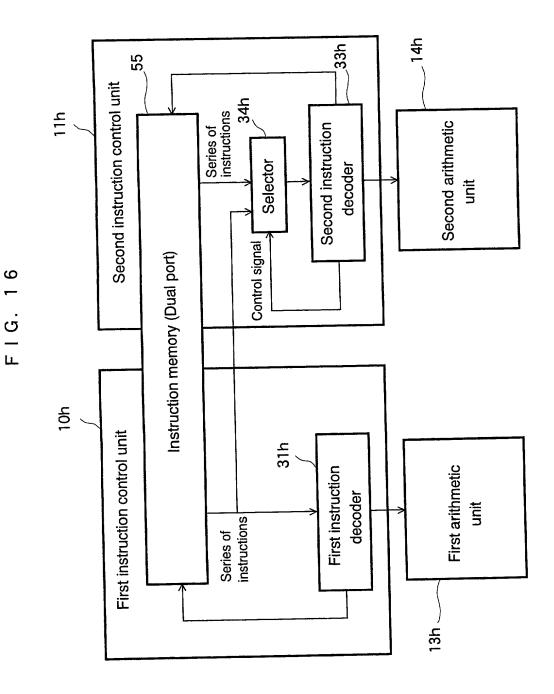
FIG. 13

Instruction control 15f 12f N-th arithmetic unit N-th instruction control unit 52 Series of time sharing instructions Switch unit 31f 13f 53 Instruction control First instruction decoder 34f First arithmetic unit Selector 10f First instruction memory 50a 0-th instruction control unit 30f First instruction control unit

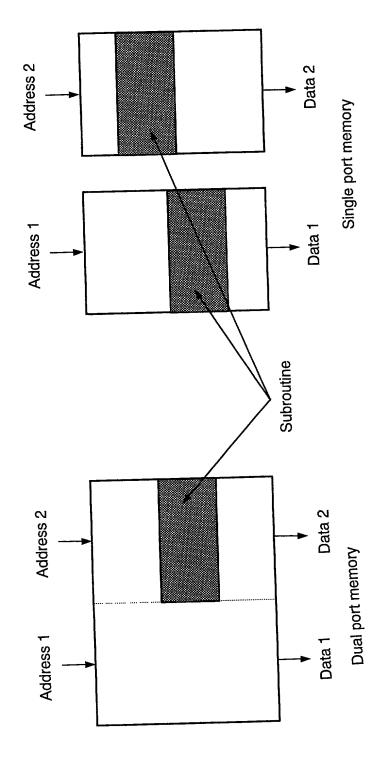
F I G.

FIG. 15





F1G. 17



F G. 18

